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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/563,298-Conf. #1328
		Filing Date	January 3, 2006
		First Named Inventor	Yoshitoshi Kida
		Art Unit	2629
		Examiner Name	R. L. Willis
Sheet 1 of 1	Attorney Docket Number	SON-3057	

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
/R.W.	BA	JP-2001-083944 A	03-30-2001	NEC IC Microcomput Syst Ltd		✓
/R.W.	BB	JP-2000-321642 A	11-24-2000	Fuji Photo Film Co Ltd		✓
/R.W.	BC	JP-07-271323 A	10-20-1995	Hitachi Ltd/Hitachi Device Eng Co Ltd		✓
/R.W.	BD	JP-02-210492 A	08-21-1990	Matsushita Electric Ind Co Ltd		

Examiner Signature	/Randal Willis/	Date Considered	02/19/2009
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NON PATENT LITERATURE DOCUMENTS			
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	CA	Japanese Office Action for application no. 2003-272250 dated July 17, 2008	

Examiner Signature		Date Considered	
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